

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandra, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING	G DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/737,638	12/14/2000		F. Scott Johnson	TI-23703.1	2828
23494	7590	10/02/2003	EXAMINER		
TEXAS INSTRUMENTS INCORPORATED				ha, Nathan W	
P O BOX 655474, M/S 3999 DALLAS, TX 75265				ART UNIT	PAPER NUMBER
,				2814	

DATE MAILED: 10/02/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
Office Action Comments	09/737,638	JOHNSON, F. SCOTT					
Office Action Summary	Examiner	Art Unit					
	Nathan W. Ha	2814					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period we Failure to reply within the set or extended period for reply will, by statute, - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	6(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from to become ABANDONEE	ely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).					
1) Responsive to communication(s) filed on 23 D	<u>ecember 2002</u> .						
2a)⊠ This action is FINAL . 2b)☐ Thi	s action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims							
4) Claim(s) 14,16 and 17 is/are pending in the ap	plication.						
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>14,16 and 17</u> is/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/or election requirement. Application Papers							
9) The specification is objected to by the Examiner							
10) The drawing(s) filed on is/are: a) □ accepted or b) □ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11) ☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved by the Examiner.							
If approved, corrected drawings are required in reply to this Office action.							
12) The oath or declaration is objected to by the Examiner.							
Priority under 35 U.S.C. §§ 119 and 120							
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) All b) Some * c) None of:							
1. Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).							
 a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121. 							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal P	(PTO-413) Paper No(s) ratent Application (PTO-152)					
S. Patent and Trademark Office							

Application/Control Number: 09/737,638 Page 2

Art Unit: 2814

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 14 and 17 are rejected under 35 U.S.C. 102(b) as being anticipated by Nakamura (US 5,290,716).

In regard to claim 14, Nakamura discloses, in fig. 1, a method of forming an emitter contact for a bipolar junction transistor comprising steps of:

providing a silicon substrate 1 having a collector region 5, an internal base region 14 within collector region, and an emitter region 18 disposed within base region;

forming a base polysilicon layer 45 on the surface of silicon substrate in contact with the base region, and defining an aperture 16 with a sidewall (not numbered) exposing the base and emitter regions of the silicon substrate;

forming the aperture through the layers;

forming a spacer 15, side walls, extending upwardly from the silicon substrate and to cover the sidewalls, the spacers covering the base region and partially covering emitter region;

forming an emitter electrode 17 positioned within the aperture in engagement with emitter region, the spacer, and the substrate, the steps include:

Art Unit: 2814

depositing a layer of emitter polysilicon 17 onto the substrate and the aperture; and

etching back the layer of emitter polysilicon, see fig. 1 and cols. 1-2.

In regard to claim 17, Nakamura further discloses pattern (etch) layers 17 and 20; see fig. 1 and col. 1, lines 50-55.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 16 is rejected under 35 U.S.C. 103(a) as being unpatentable over Nakamura as applied to claim 14 above, and further in view of Walczyk et al. (Tailoring Interface Oxide for Polysilicon..., IEEE, 1992, pp. 84-87, previously cited.)

In regard to claim 16, Nakamura discloses all of the claimed limitations as mentioned above and further discloses that the emitter region is doped with a high concentration impurity, n+, but does not expressly mention the use of in situ and rapid thermal annealing in the process of making the device. It should be noted that the method of using "in situ" and annealing is well known and have being used widely in the art of making semiconductor devices since in situ can be used to producing a minimum interfacial oxide known as "Oxide Free process", and Rapid thermal annealing process

Art Unit: 2814

can be used to avoid damaging the surface of the device. For example, Walczyk et al. evidently teaches these processes in his article of how to make an efficient semiconductor device; see page 84 col. 2 last paragraph and page 85, col. 2, first paragraph.

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention was made to use the processes as taught by Walczyk et al. in Chin in order to take advantages of these processes to improving device characteristics since in situ process can be used to producing a minimum interfacial oxide known as "Oxide Free process", and Rapid thermal annealing process can be used to avoid damaging the surface of the device.

Response to Arguments

5. Applicant's arguments with respect to claims 14, 16-17 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the

Application/Control Number: 09/737,638

Art Unit: 2814

shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nathan W. Ha whose telephone number is (703) 305-3507. The examiner can normally be reached on M-TH 8:00-7:00(EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (703) 308-4918. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Nathan Ha September 24, 2003

SUT The sum of the sum

Page 5